



Attorney's Docket No. 042390P9490C
Confirmation No.: 1032

PATENT

AFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Anand Murthy, et al.

Serial No.: 10/626,257

Filed: July 23, 2003

For: A SEMICONDUCTOR TRANSISTOR
HAVING A STRESSED CHANNEL

Mail Stop AF
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Examiner: Soward, Ida M.

Art Unit: 2822

FIRST CLASS CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to MS: AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

On: March 3, 2005

By: Dianne Neathery
Dianne Neathery

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Transmitted herewith is a complete set of formal drawings, comprising three (3) sheets and seven (7) figures for the above-identified application.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: March 3, 2005

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